

JSPM's
RAJARSHI SHAHU COLLEGE OF ENGINEERING,PUNE-33

DEPARTMENT OF E & TC ENGINEERING

ACADEMIC YEAR: 2023-24

SEMESTER I

**Report Of “Workshop on Software Development Tool for Circuit Design” under
ENTESA Association with IETE**

Class : SY E & TC

Name of Session : Workshop on Software Development Tool for Circuit Design

Date & Day : 31st Jan 2024 [Wednesday]

Total No. of students : 120

Name of speaker : Mr. Shridhar S. Dudam

Name of Industry : Smart Logic Technologies,Pune

Staff Members

who have attended : Dr. P. M. Ghate & Prof. S. P. Pattanaik

PO attained: PO1,PO2,PO3,PO4,PO5,PO12

Topic of Discussion :

- 1. Highlighted about the Scope of Proteus Software.**
- 2. Discussed about the Proteus Software Basics.**
- 3. Discussed about the different libraries of Proteus Software.**
- 4. Explained about how it is useful as a simulation tool.**
- 5. Explained its usage by taking no of interface circuits.**
- 6. Took lots of practices of various interfacing with PIC Microcontroller.**
- 7. Students also performed different interfacing circuits in Proteus Software.**

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DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION

SOFTWARE DEVELOPMENT TOOL FOR CIRCUIT DESIGN

WORKSHOP UNDER ENTESA STUDENTS ASSOCIATION



MR. SHRIDHAR S. DUDAM
Chief Technology Officer,
Smart Logic Technologies, Pune

31 JAN, 2024

FACULTY CO-ORDINATOR: MRS. SWAPNALINI PATTANIAK
FACULTY ADVISOR: DR. P. M. GHATE
HEAD OF DEPARTMENT: DR. S. C. WAGAJ







Dr. P. M. Ghate
Event Co-ordinator

Prof. S. P. Pattanaik
Event Co-ordinator

Dr. S. C. Wagaj
HOD

